

Amendments to the Claims:

Please cancel claims 12 and 13 and amend claims 11, 14 and 16 as shown in the following listing of claims. This listing of claims will replace all prior versions and listings of claims in the application.

1-10. (cancelled)

11. (currently amended) An apparatus for providing an adjustable bandwidth high pass filter, the apparatus comprising:

a high pass filter having an input capacitor coupled in series with a resistive ladder having a plurality of resistors coupled in series, the coupling between the capacitor and a first resistor of the resistive ladder defining a first tap and successive couplings between resistors forming successive taps, a last resistor of the resistive ladder being coupled to a ground;

a plurality of bandwidth adjusting resistors, each having a first side coupled to the first tap; and

a plurality of MOSFET devices, each MOSFET device having a source, a gate and a drain, the source of each MOSFET device coupled to a second side of one of a corresponding bandwidth adjusting resistor;

the drain of each MOSFET device coupled to said ground and the gate being coupled to a corresponding control circuit, each control circuit comprising an amplifier output coupled to the gate of the corresponding MOSFET device, each amplifier comprising a tristate buffer amplifier and a pull-up resistance coupled between the output of the tristate buffer amplifier and a power supply.

Claims 12-13 (cancelled)

14. (currently amended) The apparatus as in claim ~~13~~11, wherein the power supply is the power supply for an integrated circuit containing the apparatus.

15. (previously presented) The apparatus as in claim 13, wherein the pull-up resistance comprises a long channel triode device.

16. (currently amended) ~~The apparatus as in claim 11, further comprising~~ An apparatus for providing an adjustable bandwidth high pass filter, the apparatus comprising:

a high pass filter having an input capacitor coupled in series with a resistive ladder having a plurality of resistors coupled in series, the coupling between the capacitor and a first resistor of the resistive ladder defining a first tap and successive couplings between resistors forming successive taps, a last resistor of the resistive ladder being coupled to a ground;

a plurality of bandwidth adjusting resistors, each having a first side coupled to the first tap;

a plurality of MOSFET devices, each MOSFET device having a source, a gate and a drain, the source of each MOSFET device coupled to a second side of one of a corresponding bandwidth adjusting resistor, the drain of each MOSFET device coupled to said ground and the gate being coupled to a corresponding control circuit; and

a plurality of capacitors, each disposed between the gate and source of a corresponding MOSFET device.

17. (previously presented) The apparatus as in claim 15, wherein the drain of the long channel triode device is coupled to a power supply voltage, the gate to the ground of the power supply voltage and the source is coupled to the gate of the corresponding MOSFET device.